

16

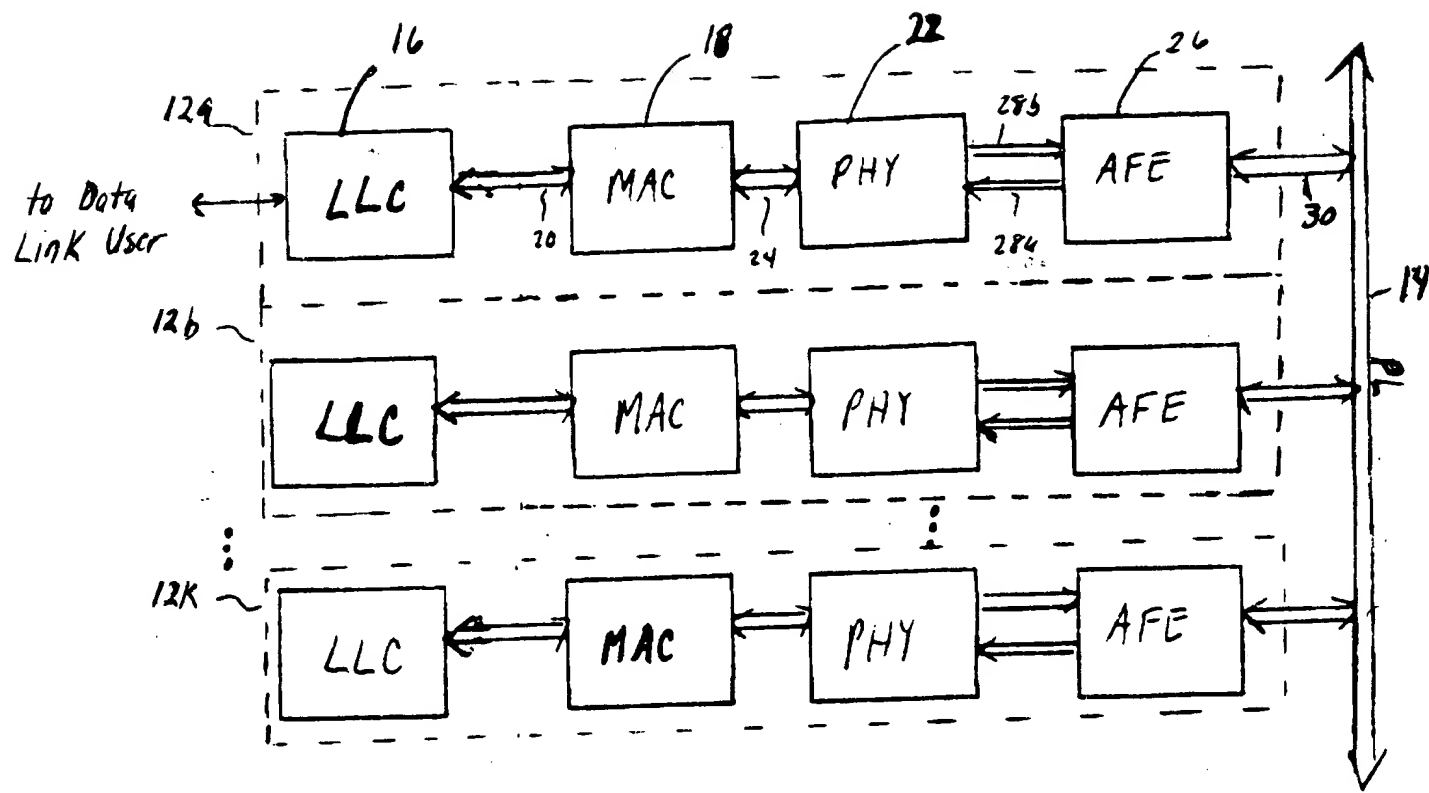


FIG. 1

004000-2692960

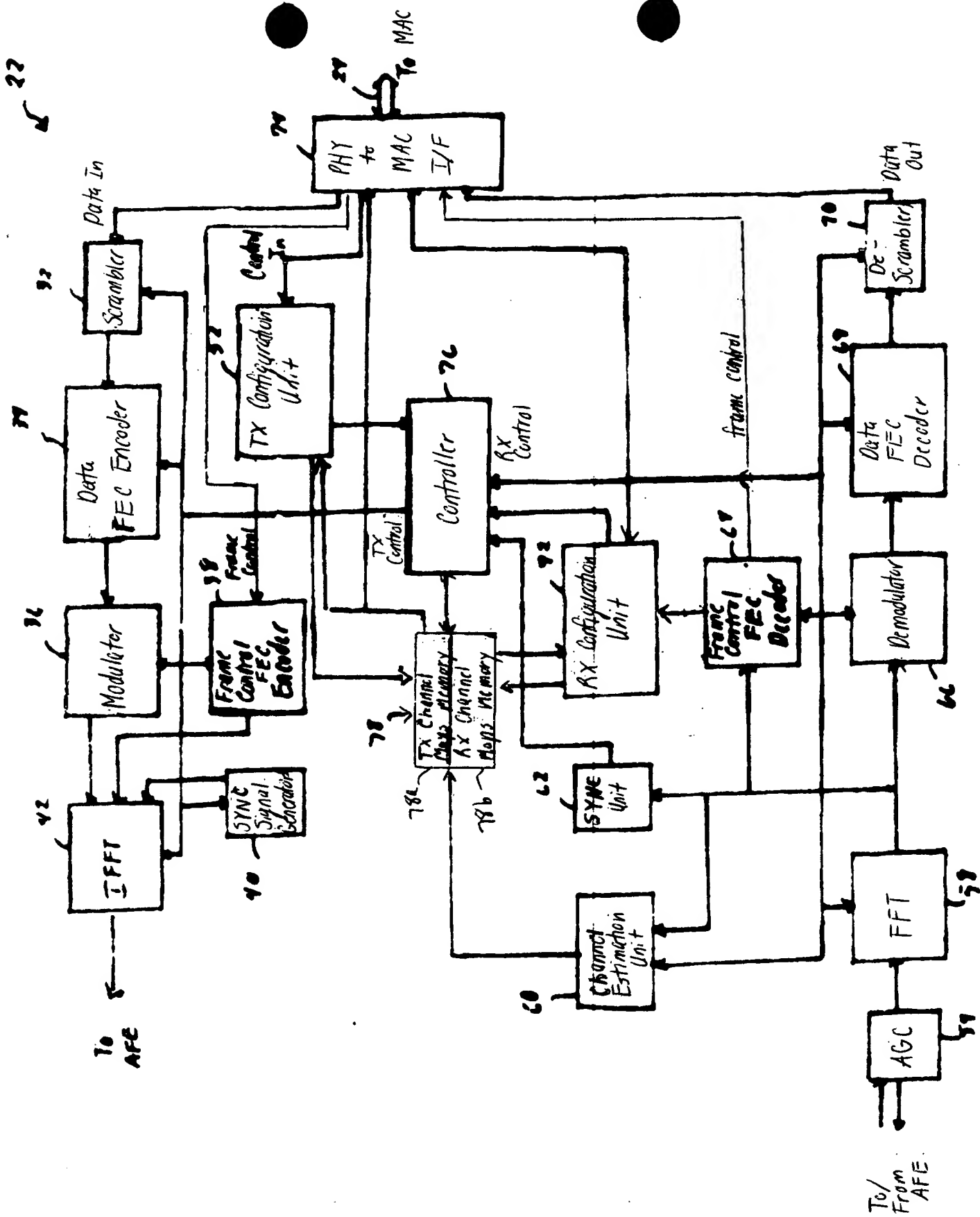


FIG. 2

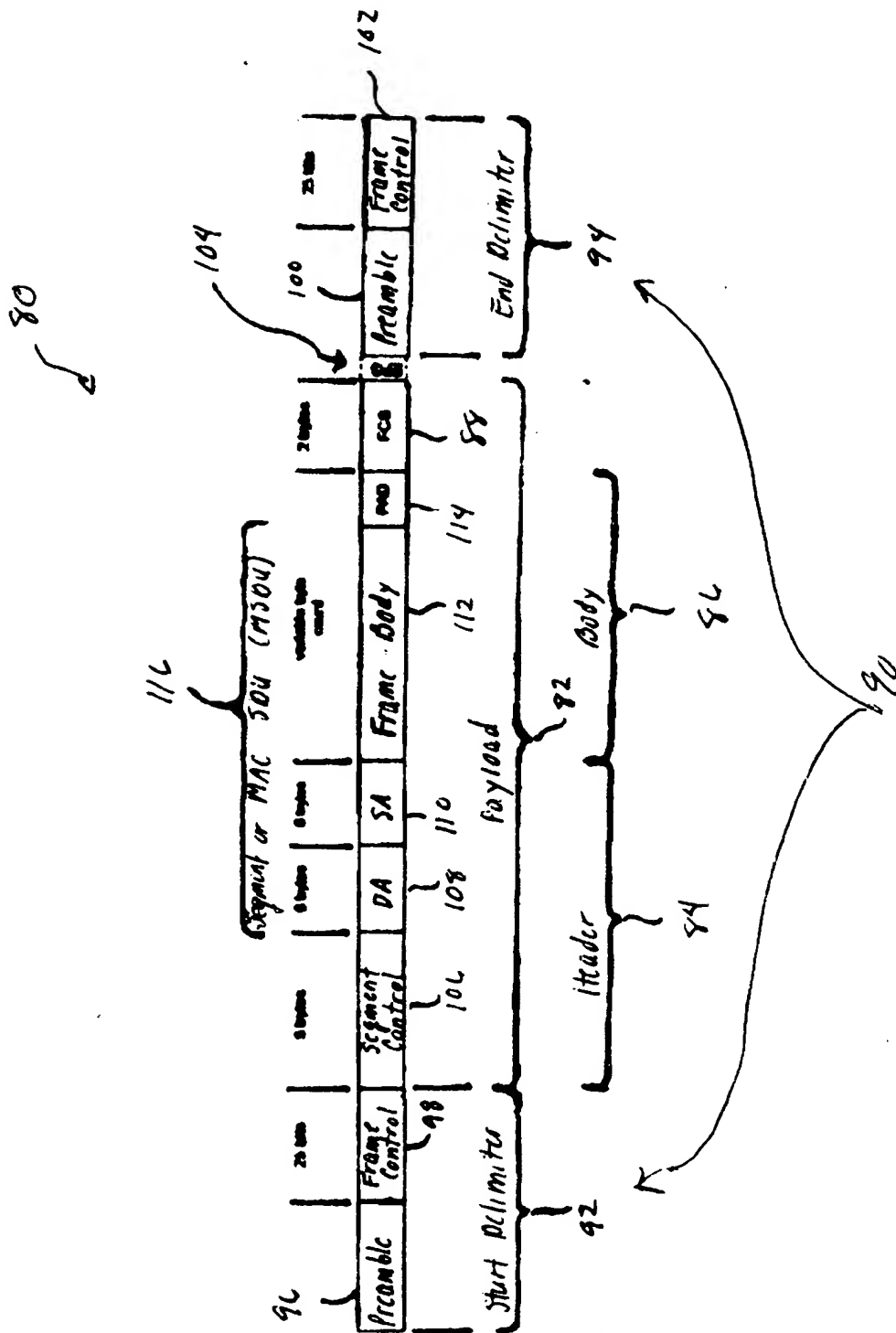


FIG. 3

120 ✓

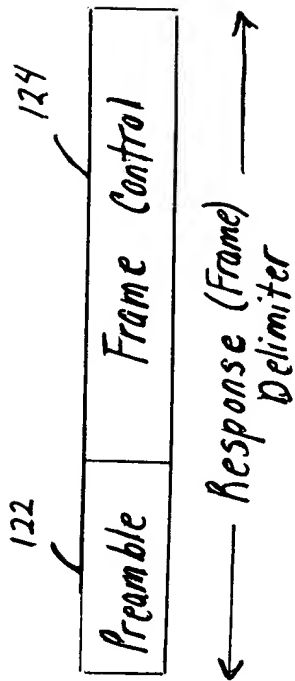


FIG. 4

|     |  |     |  |     |  |      |  |
|-----|--|-----|--|-----|--|------|--|
| CC  |  | DT  |  | VF  |  | FCCS |  |
| 130 |  | 132 |  | 134 |  | 136  |  |

FIG. 5A

[illegible]

102

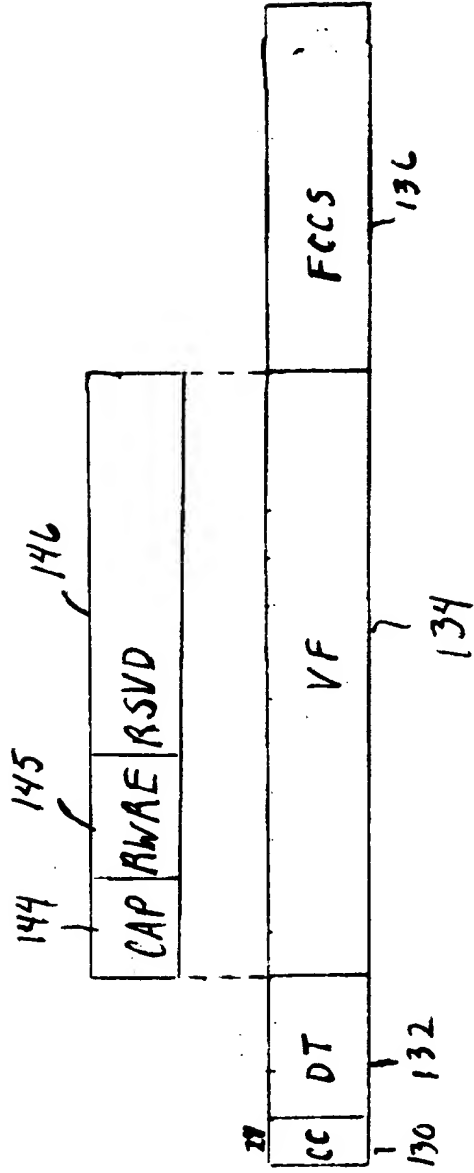


FIG 5B

Diagram illustrating a packet structure with fields and bit positions:

- Field 1 (FTPE):** Bit position 149.
- Field 2 (RSVD):** Bit position 150.
- Field 3 (RFCS):** Bit position 148.
- Field 4 (CAP):** Bit position 144.
- Field 5 (ACK):** Bit position 146.
- Field 6 (AFF):** Bit position 145.
- Field 7 (VF):** Bit position 131.
- Field 8 (DT):** Bit position 132.
- Field 9 (FCCS):** Bit position 130.

Additional bit positions indicated: 124, 134, 136.

6

106 ↗

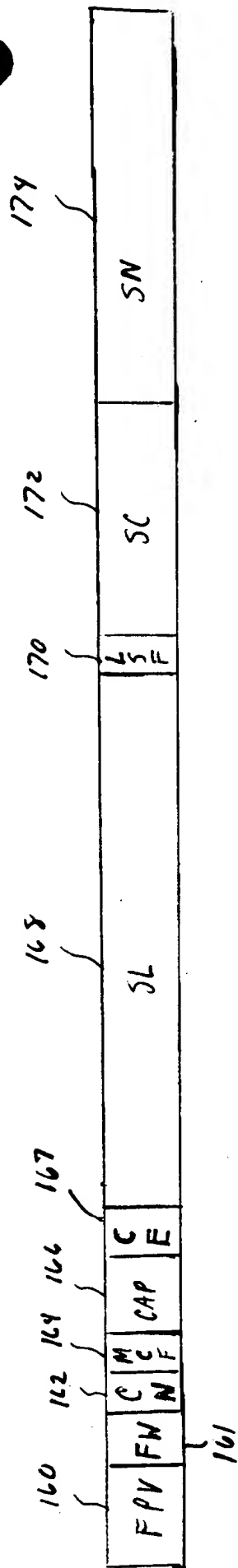


FIG. 7



112

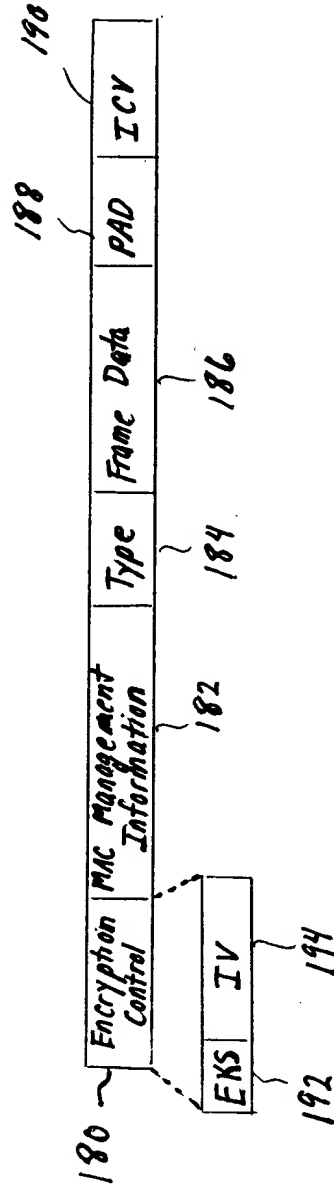


FIG. 8

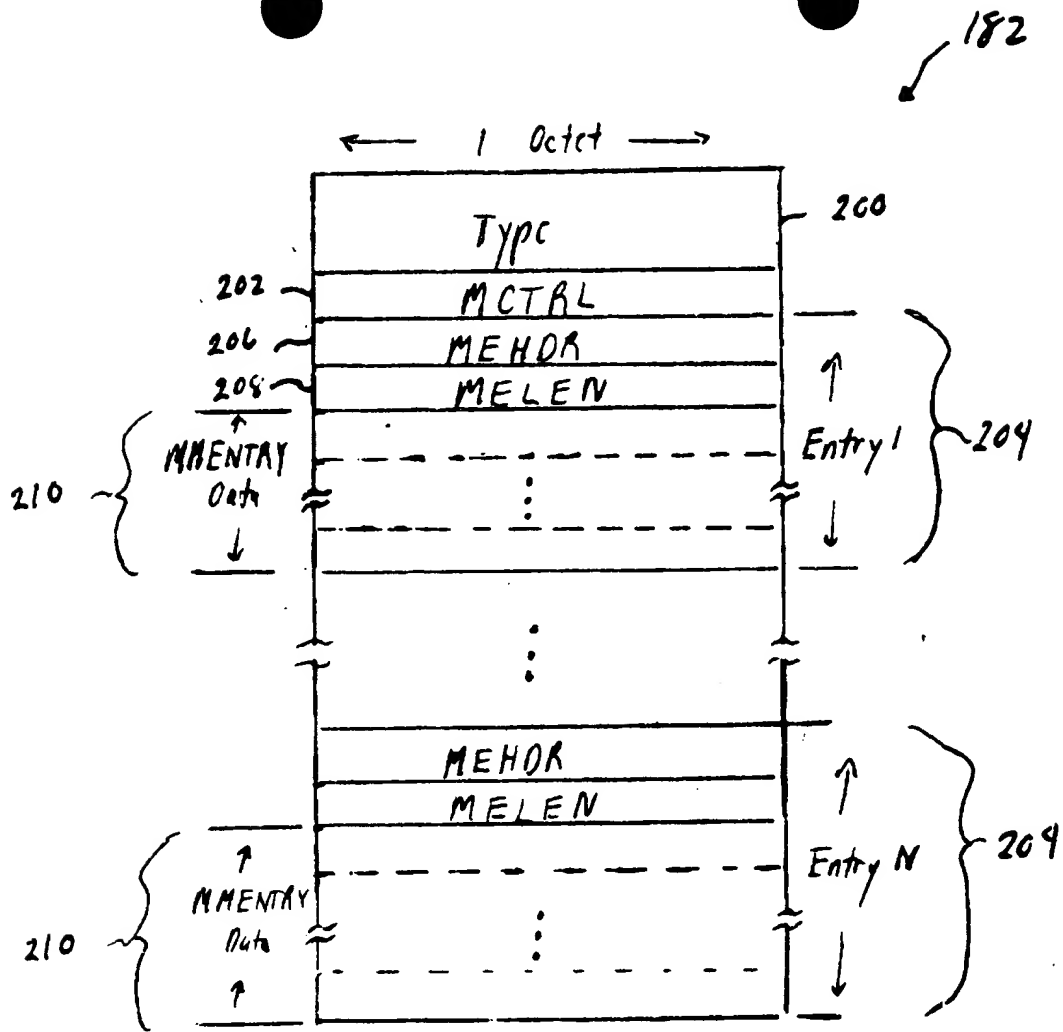


FIG. 1

Diagram of a data structure 202. It is a horizontal rectangle divided into two sections. The left section is labeled 212 and contains the text "ASVD". The right section is labeled 219 and contains the text "NE". A bracket labeled 202 points to the entire structure.

FIG. 10



FIG. 11

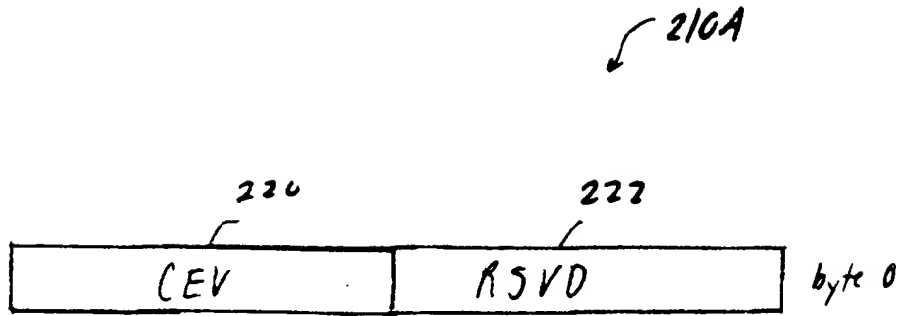


FIG. 12A

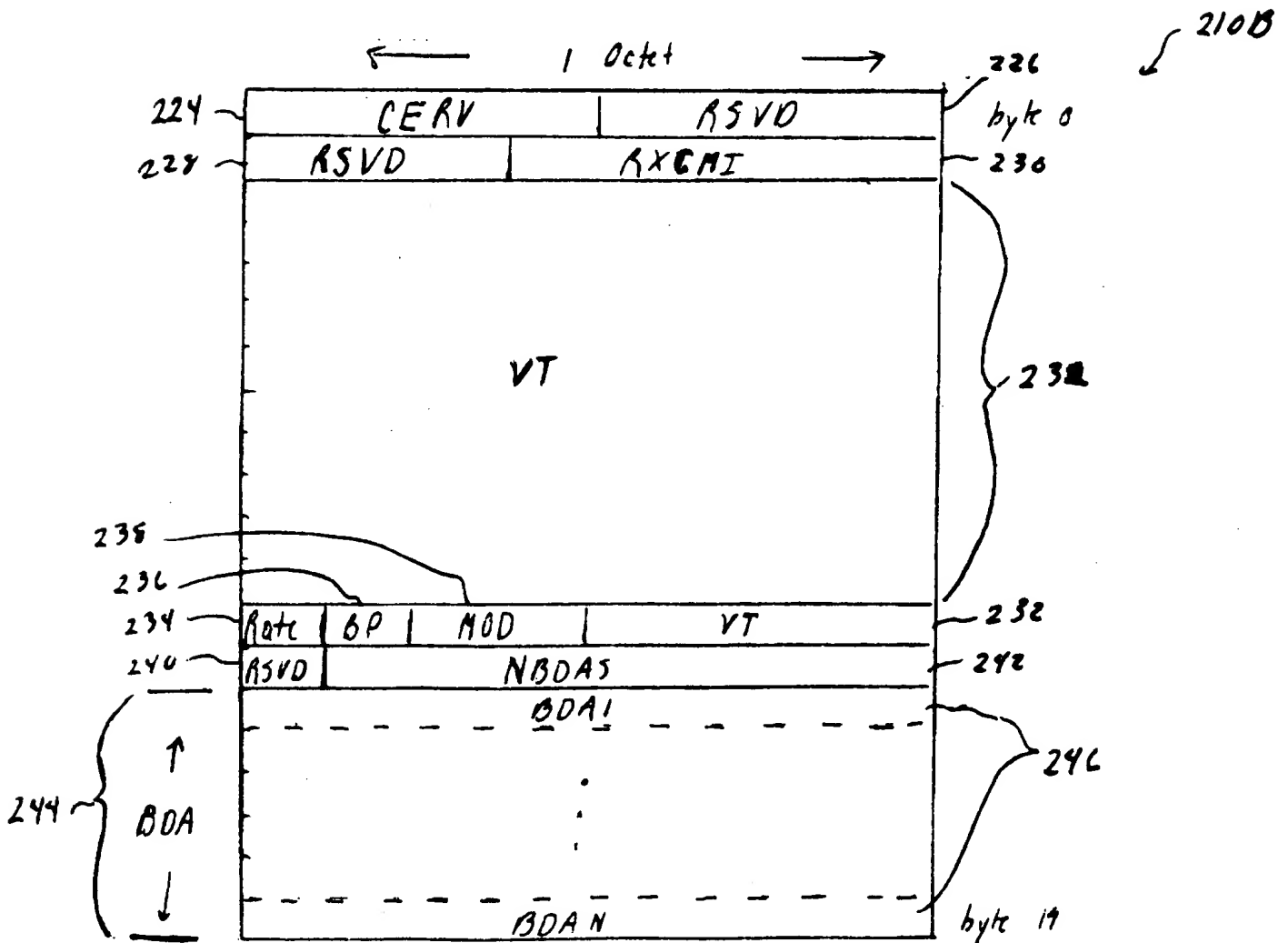


FIG. 12B

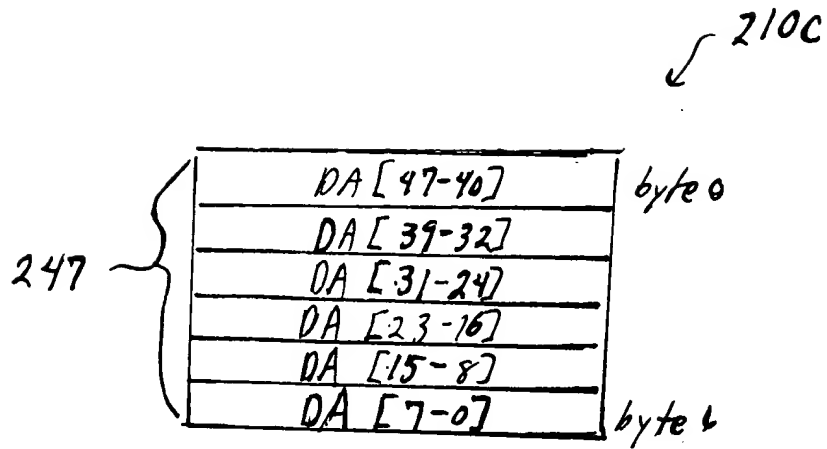


FIG. 13A

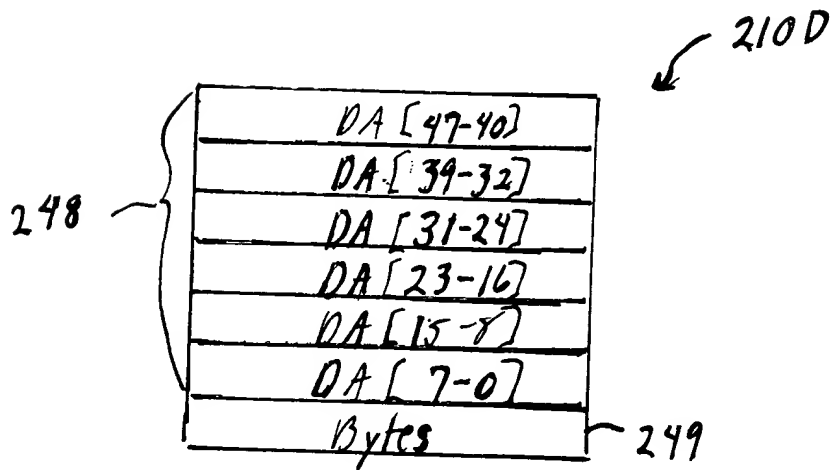


FIG. 13B

210 E

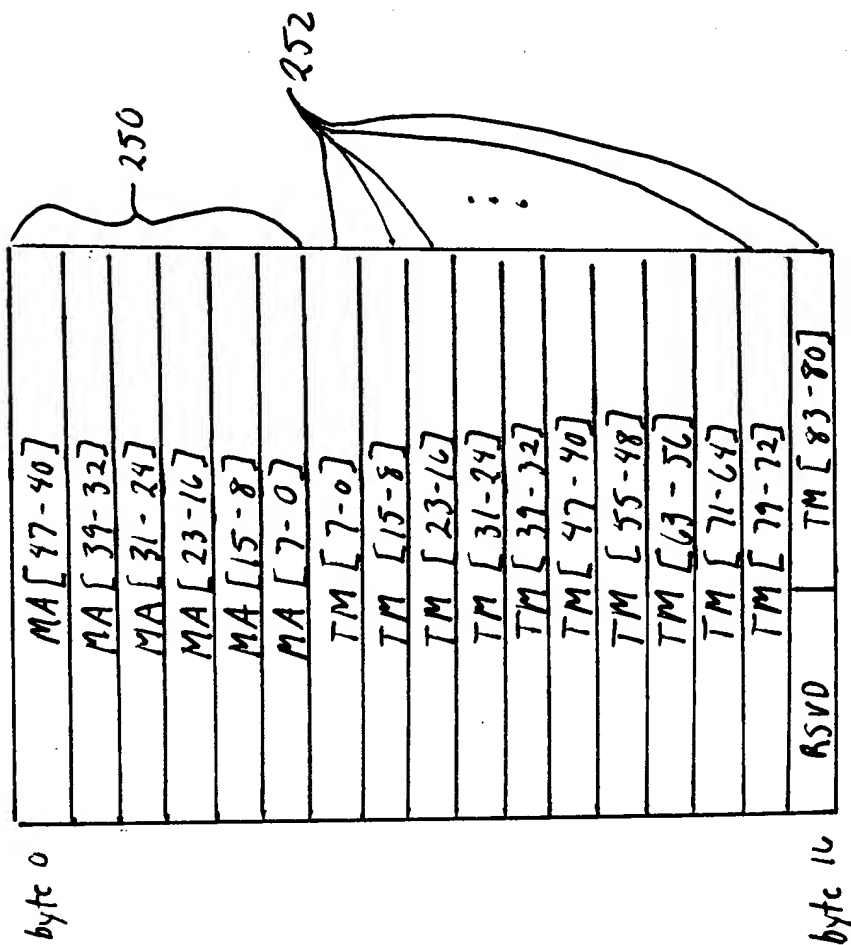


FIG. 14

210F

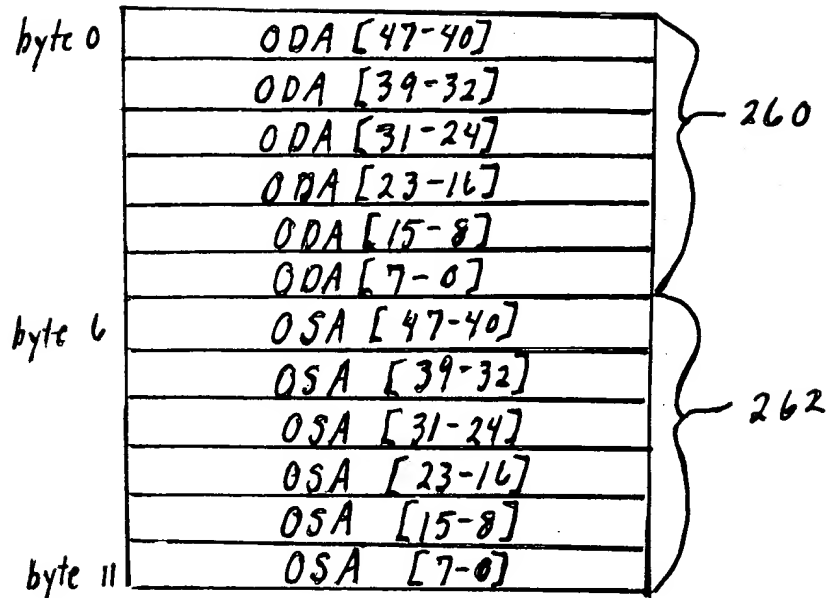


FIG. 15

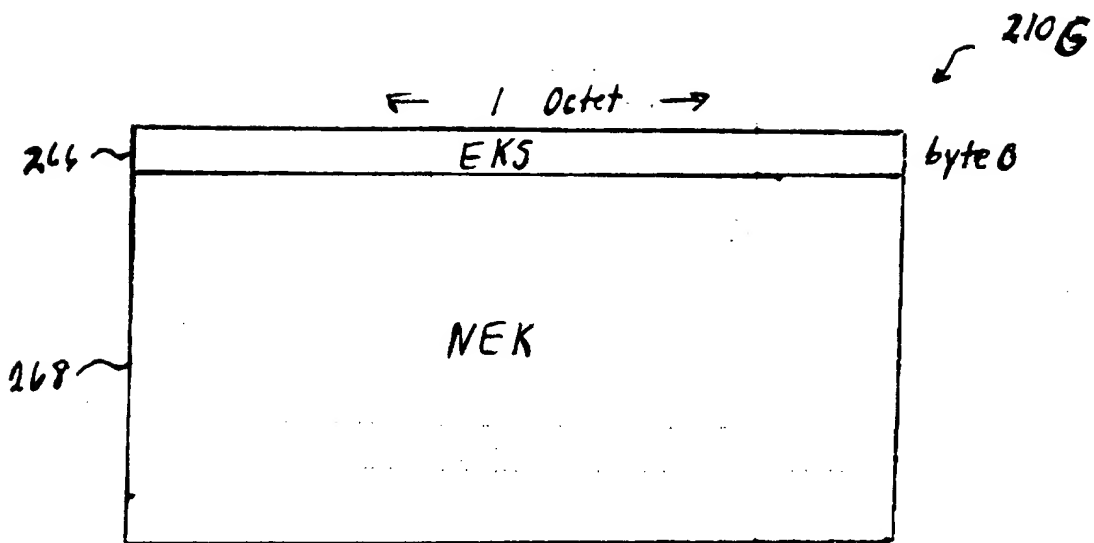


FIG. 16





—

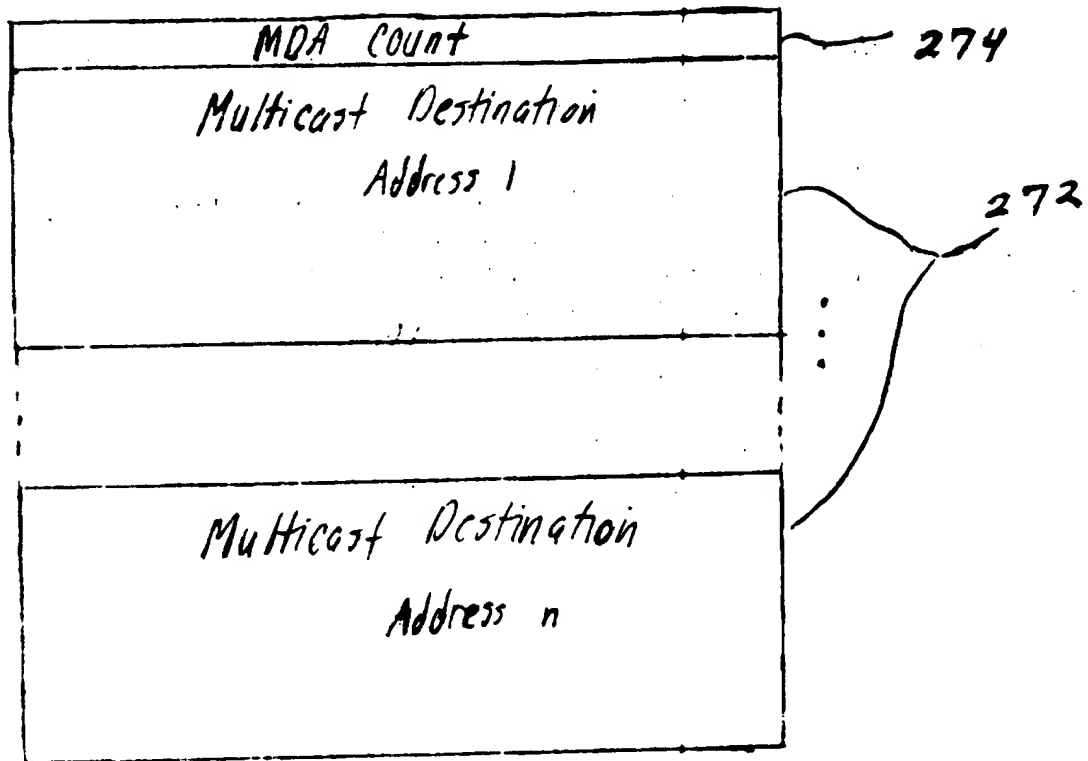


FIG. 17

2025 RELEASE UNDER E.O. 14176

210I

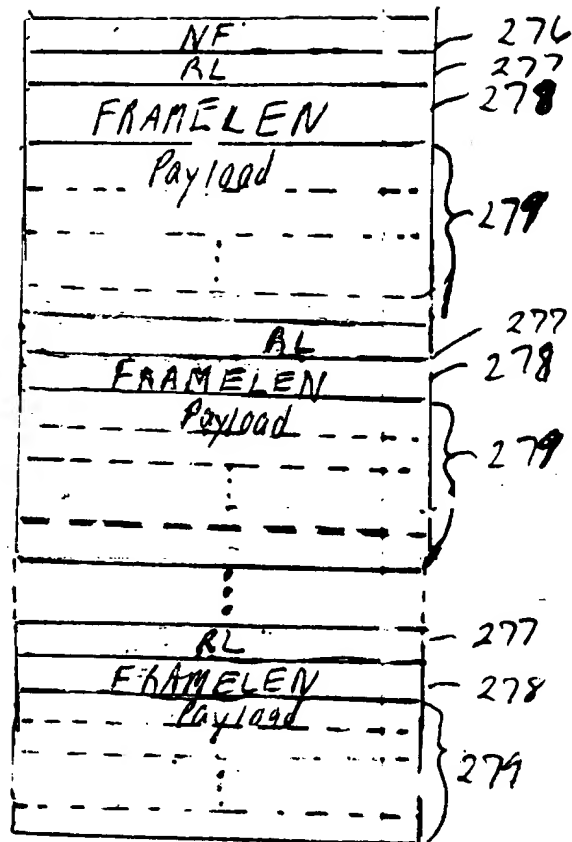


FIG. 18

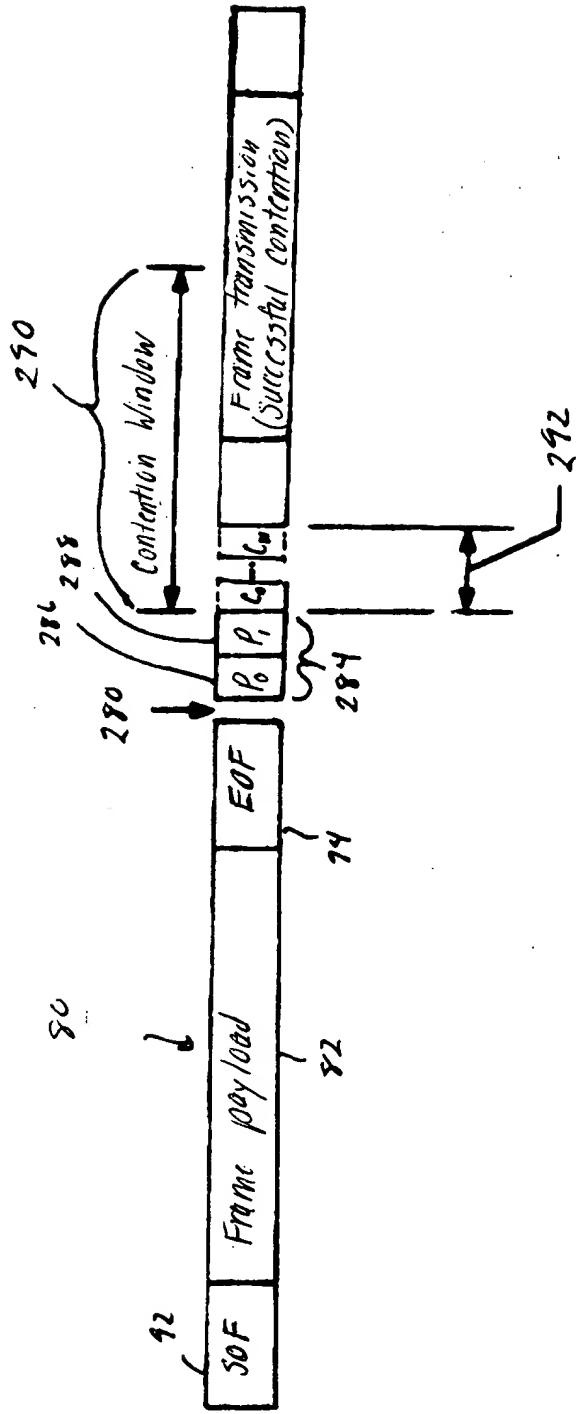


FIG. 19A

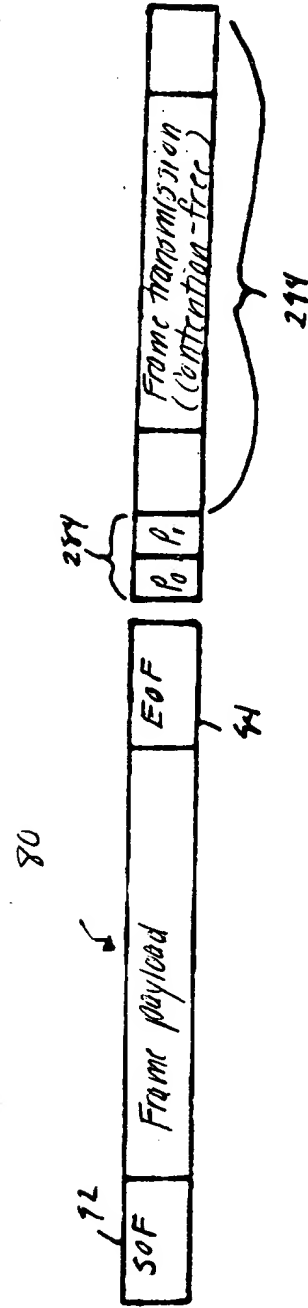


FIG. 19B





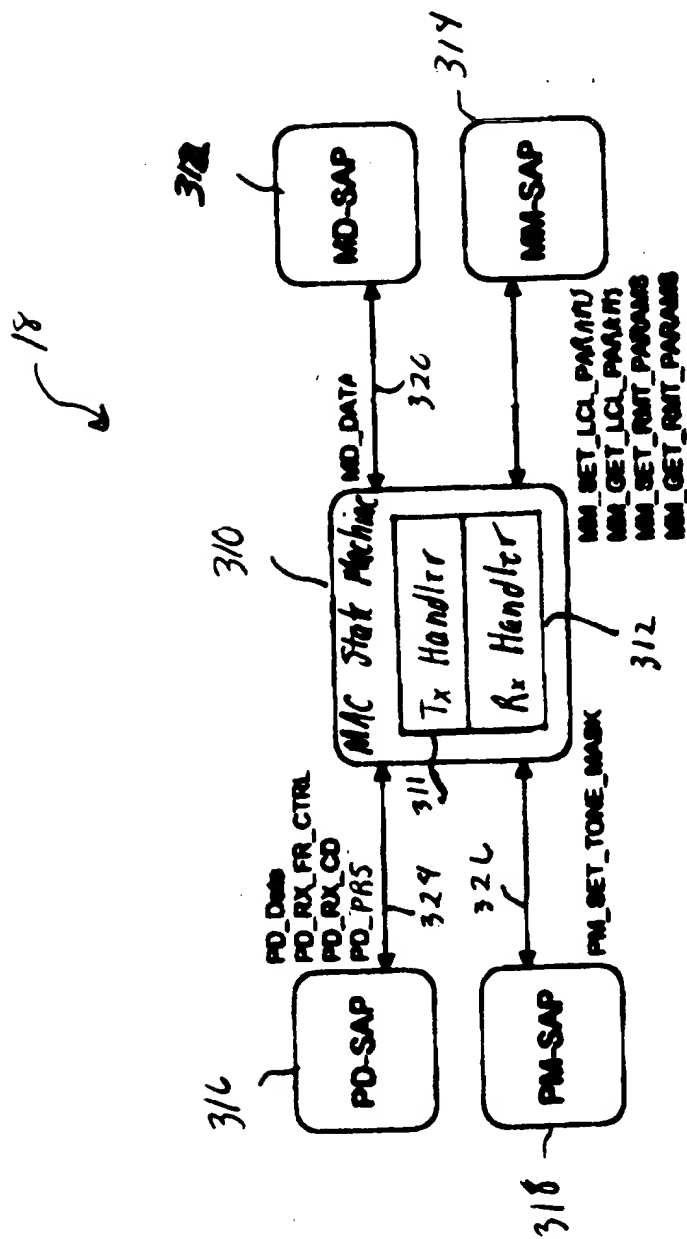


FIG. 21

311

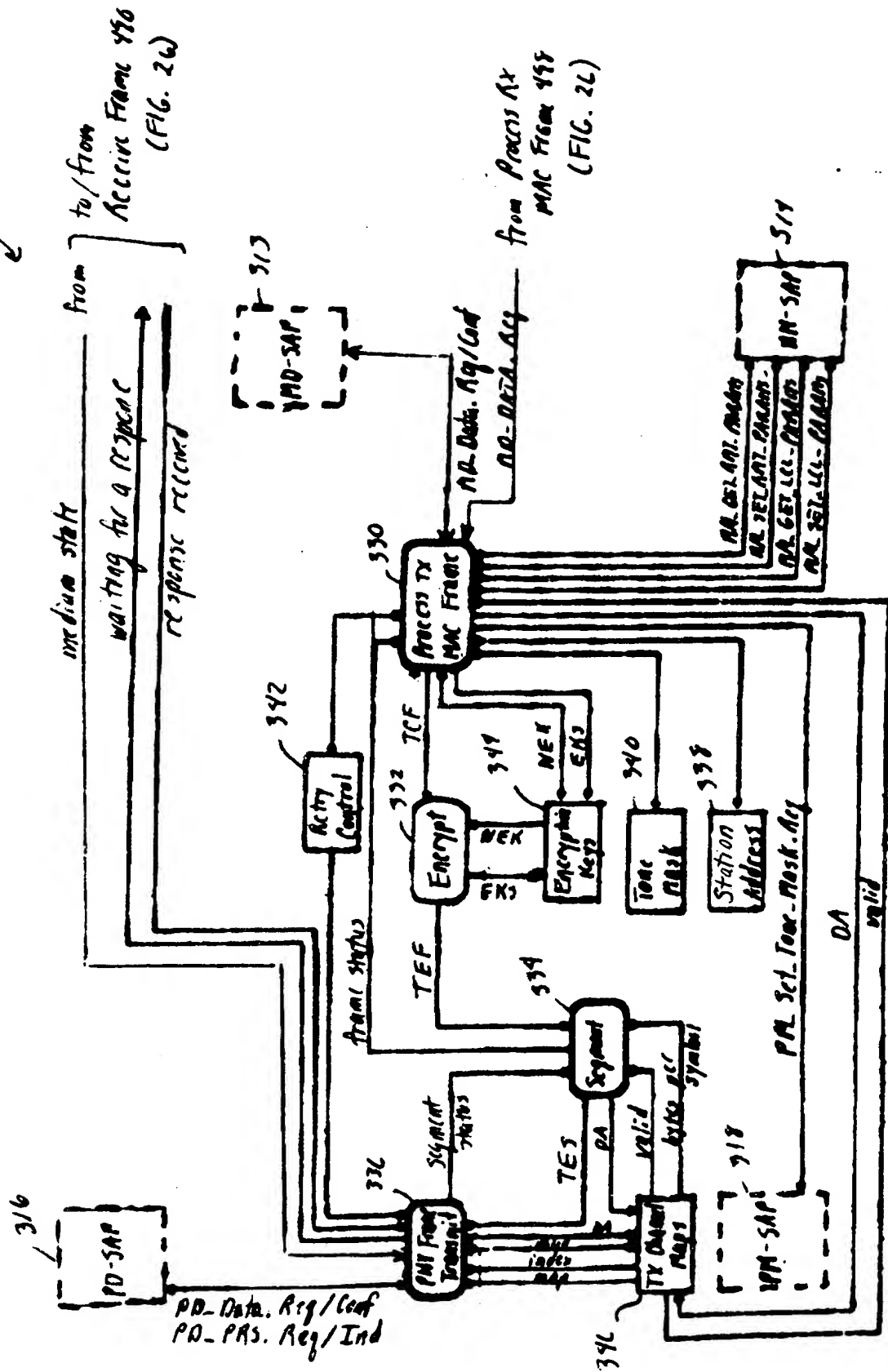


FIG. 22

400

Frame  
Arrival

402

BPC=0; NRC=0  
 TC=0;  
 NACKcount=0;  
 FrmTimer=MaxLife;  
 Priority=0-3

403  
 VCS=0  
 CS=0?

407

In CSS  
 Interval

409

In PRS  
 interval

423

In Contention  
 Window

Transmit segment,  
 Increment  
 TC

428

Ack  
 expected?

430

Valid Ack  
 received?

Resolve  
 response

432

More  
 segments?

442

Indicate  
 Success

440

End

433  
 BPC=0; NRC=0;  
 TC=0;  
 NACKcount=0

436

FrmTimer=0  
 or TC>  
 limit

Indicate frame  
 dropped

438

FIG. 23

336

Wait for VCS=0  
 + CS=0;  
 Update VCS, VPF  
 + CC on  
 frame control

404

405  
 VPF=1

408  
 Signal  
 sensed in  
 CSS

410  
 CC=1?

412  
 Contend  
 or Interrupt  
 Transmission

416  
 Signal/Listen  
 in PRS

418  
 Heard  
 higher  
 priority?

419  
 Contend  
 for Channel

421  
 Valid  
 frame  
 control?

422  
 Update VCS  
 VPF=1

414  
 Set  
 VCS=EIFS,  
 VPF=0



from Step 430 (FIG. 23)

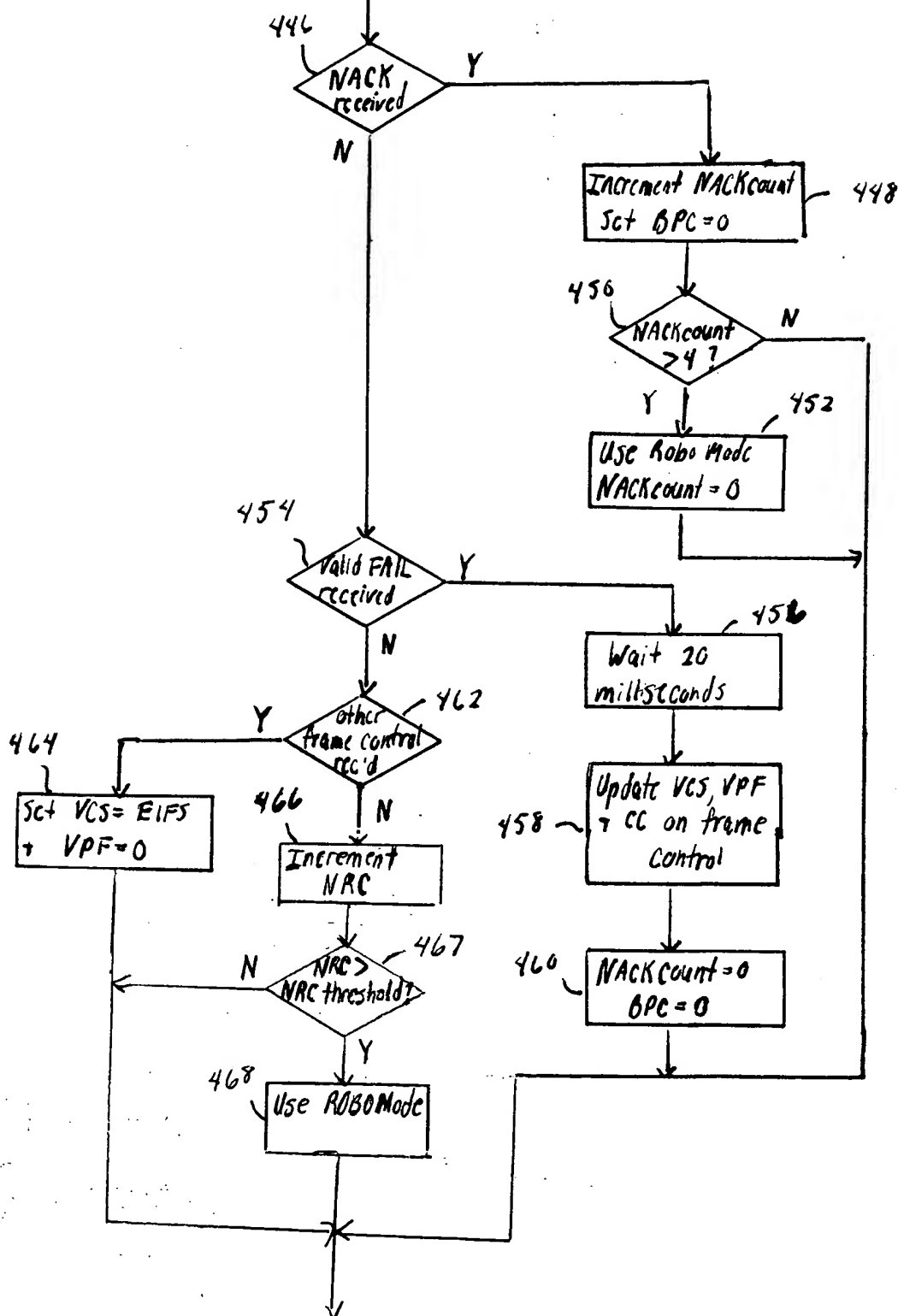


FIG. 24

From Step 418 (FIG. 23)

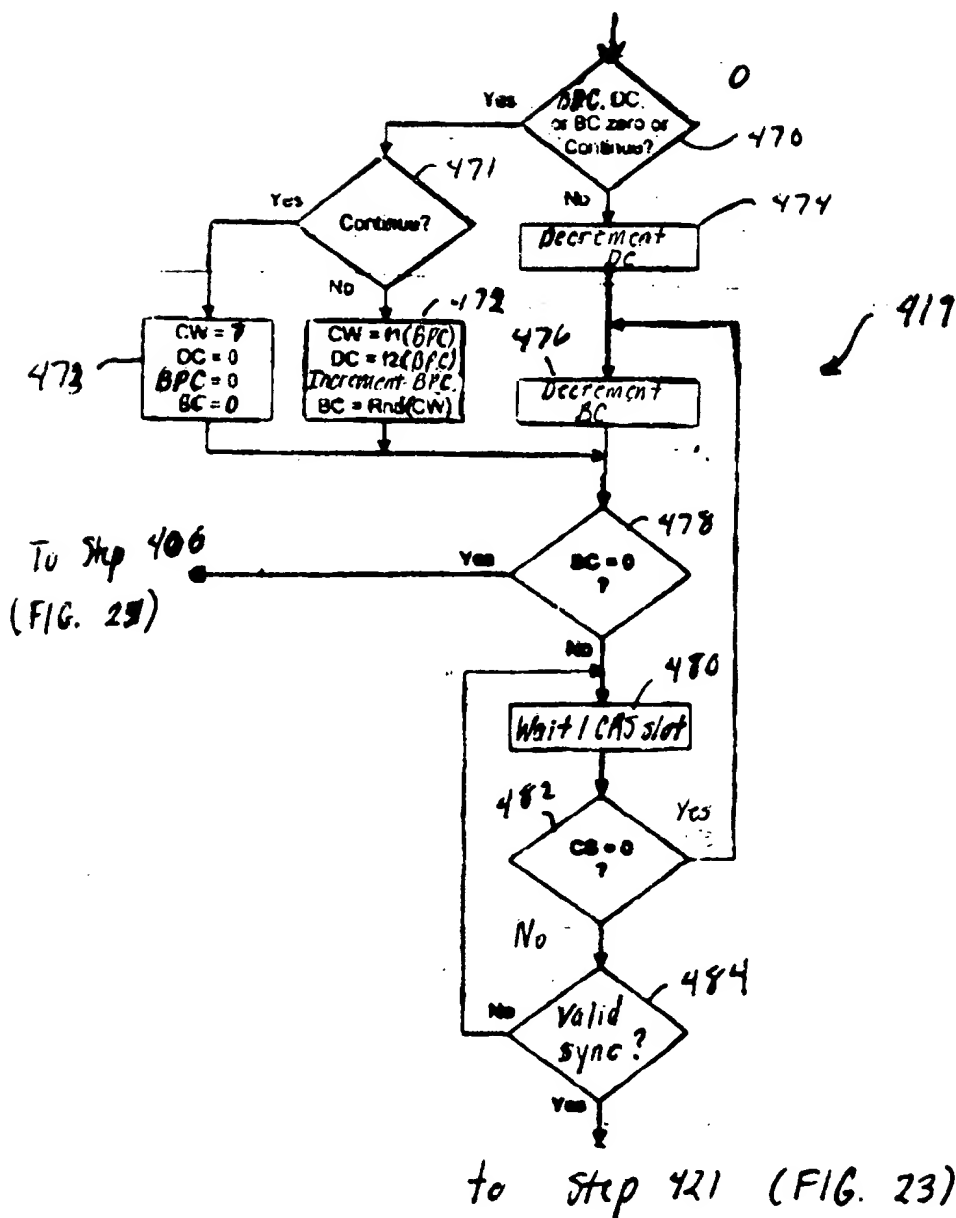


FIG. 25

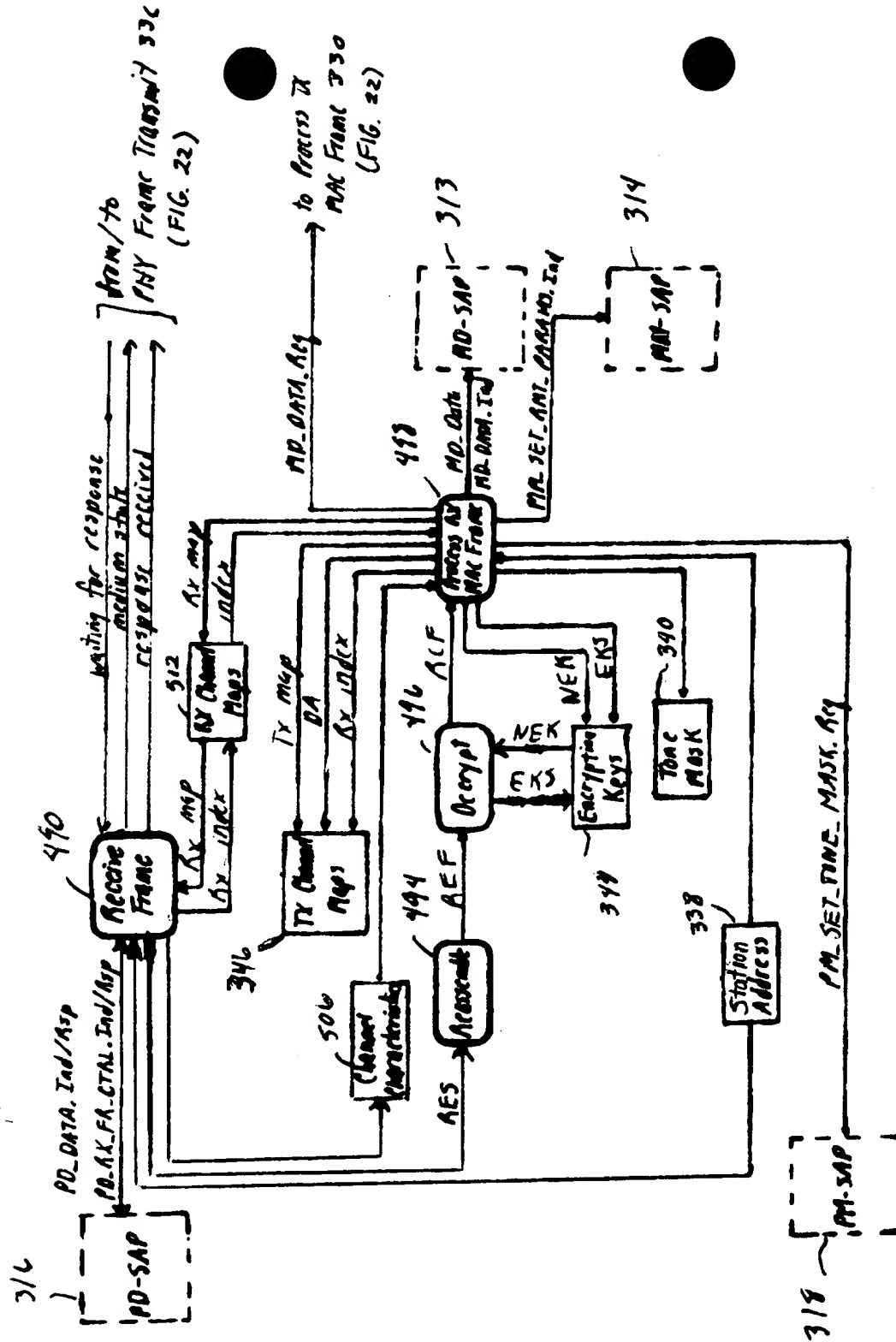
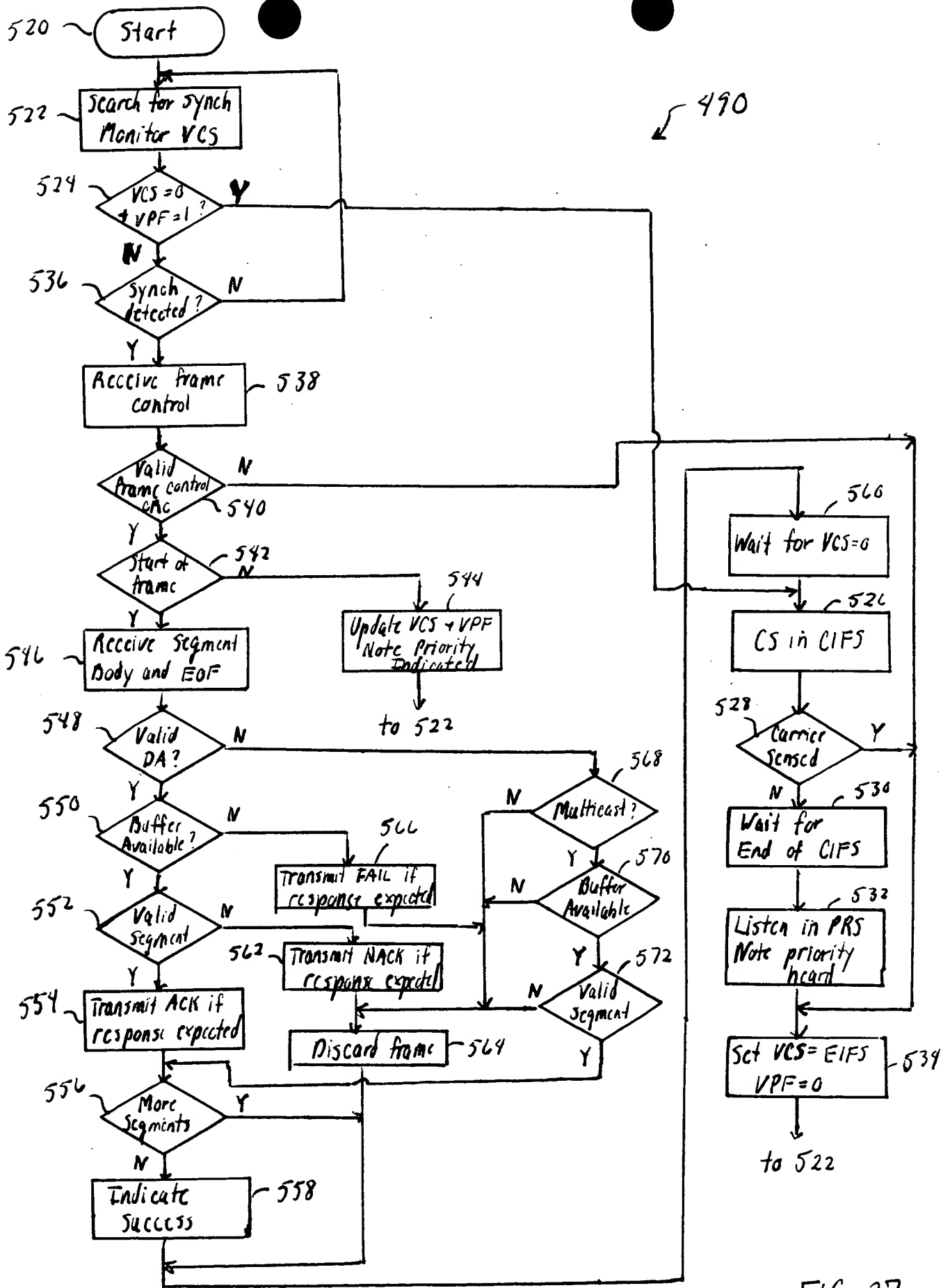
[illegible]

FIG. 26



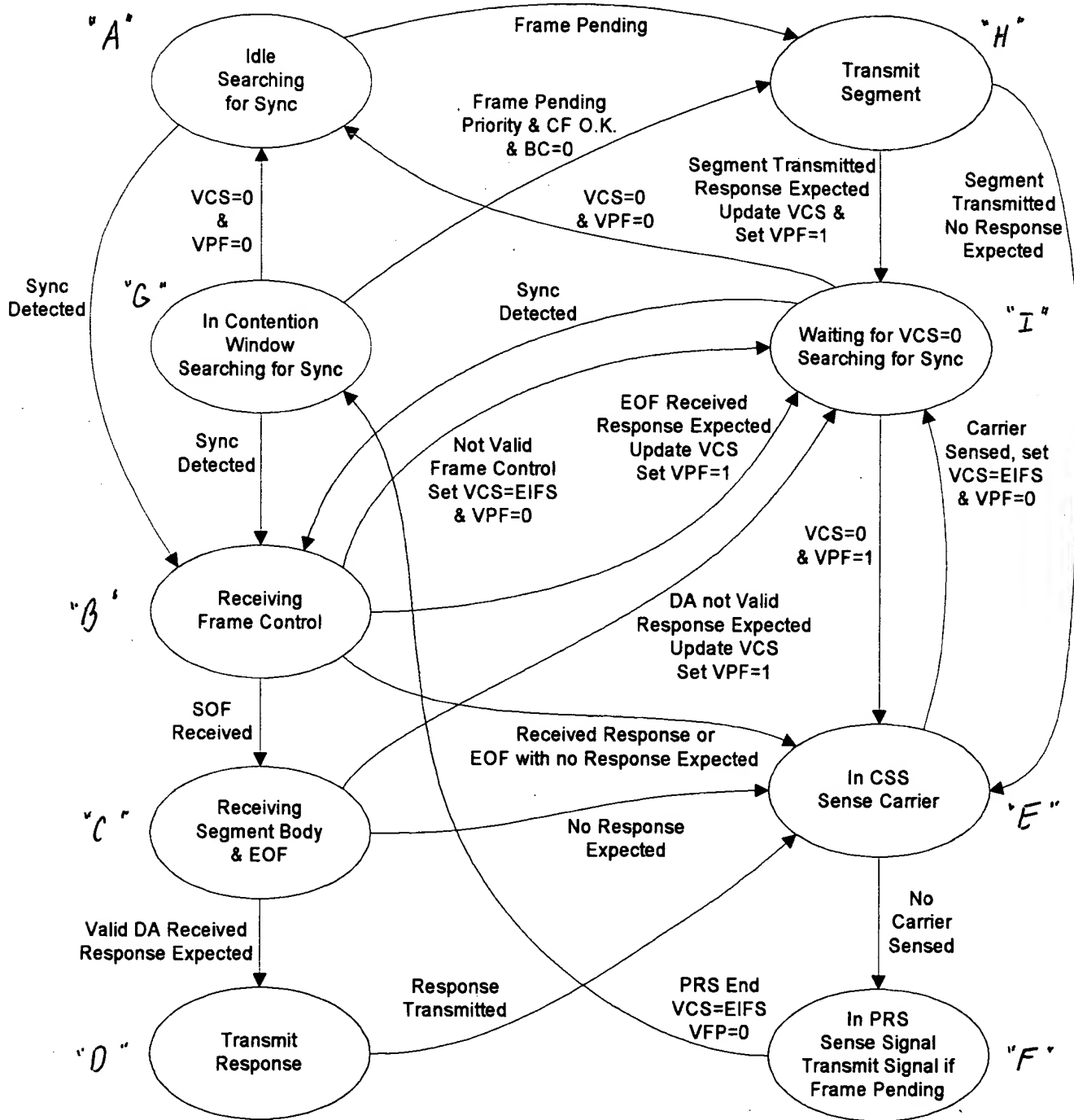


FIG. 28

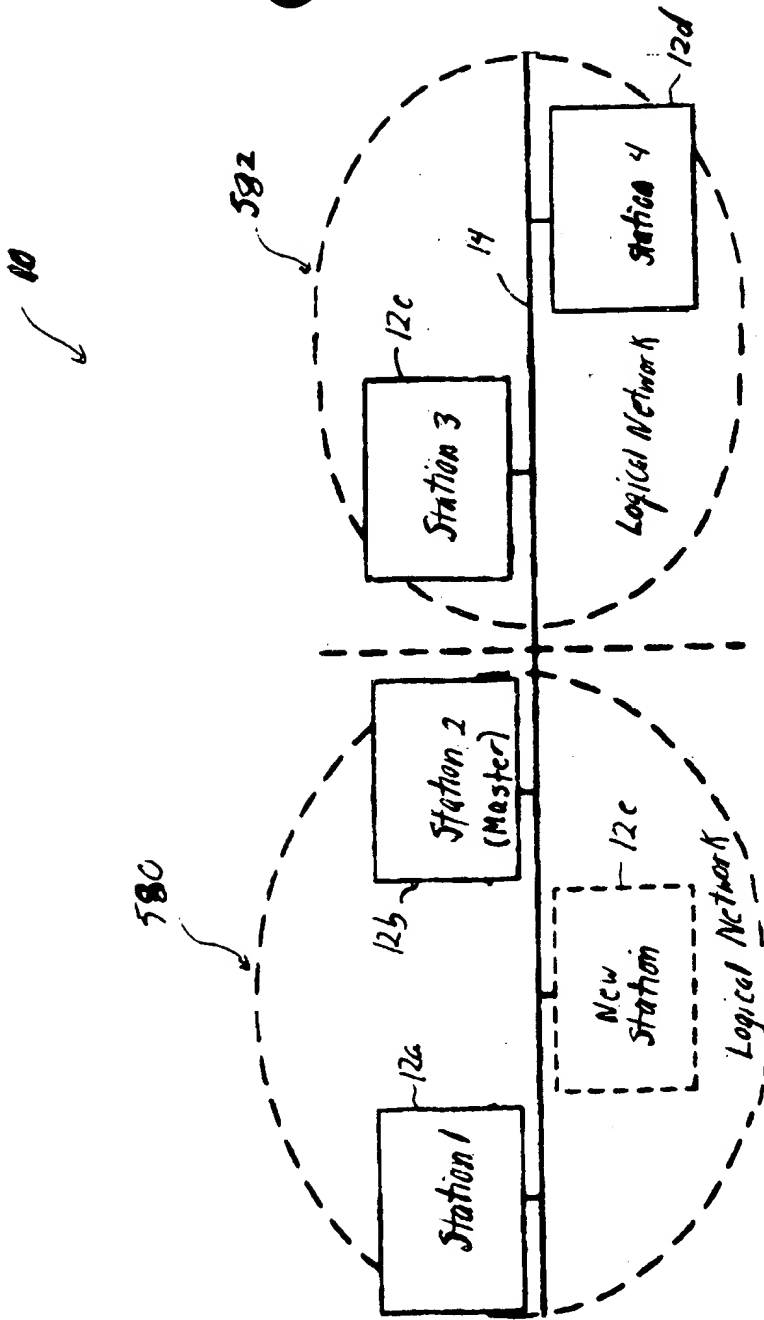


FIG. 29

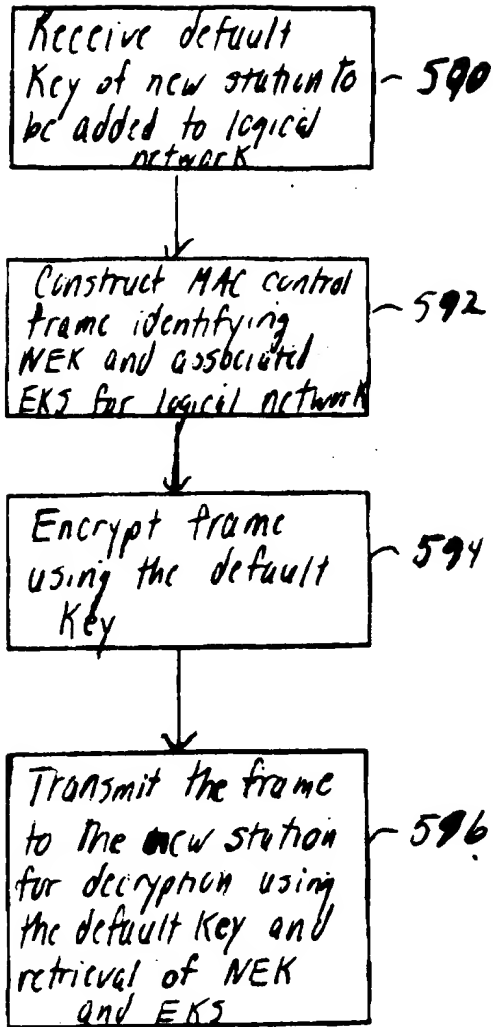


FIG. 30





*(The following text is extremely faint and appears to be bleed-through from the reverse side of the page. It contains several lines of illegible text.)*

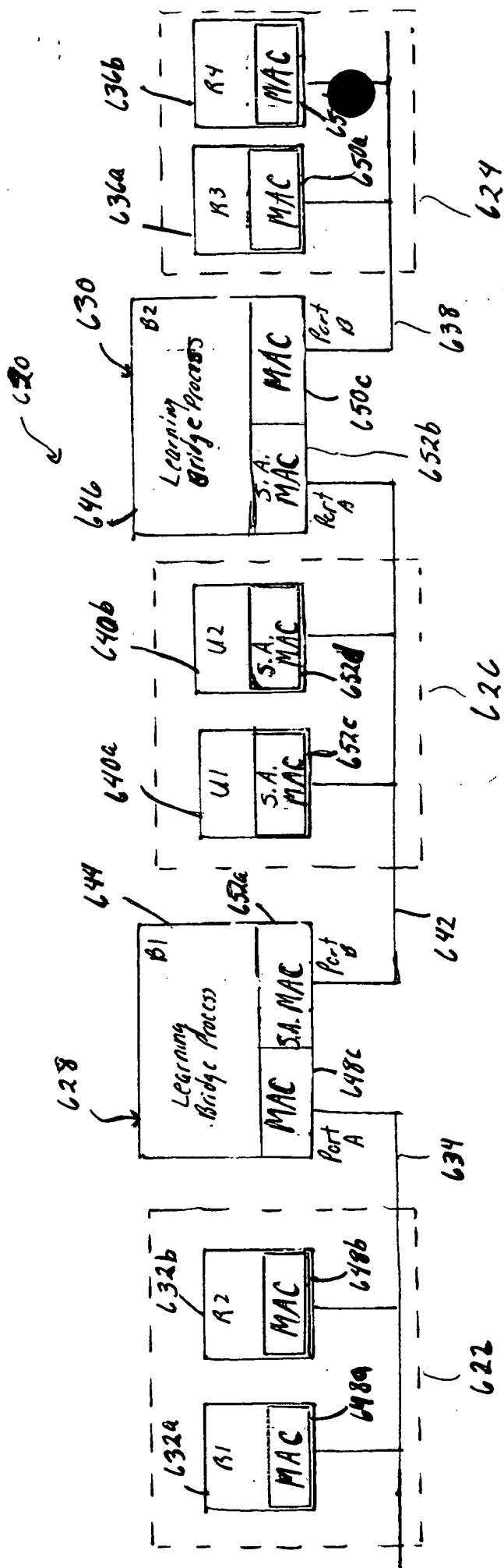


FIG. 32

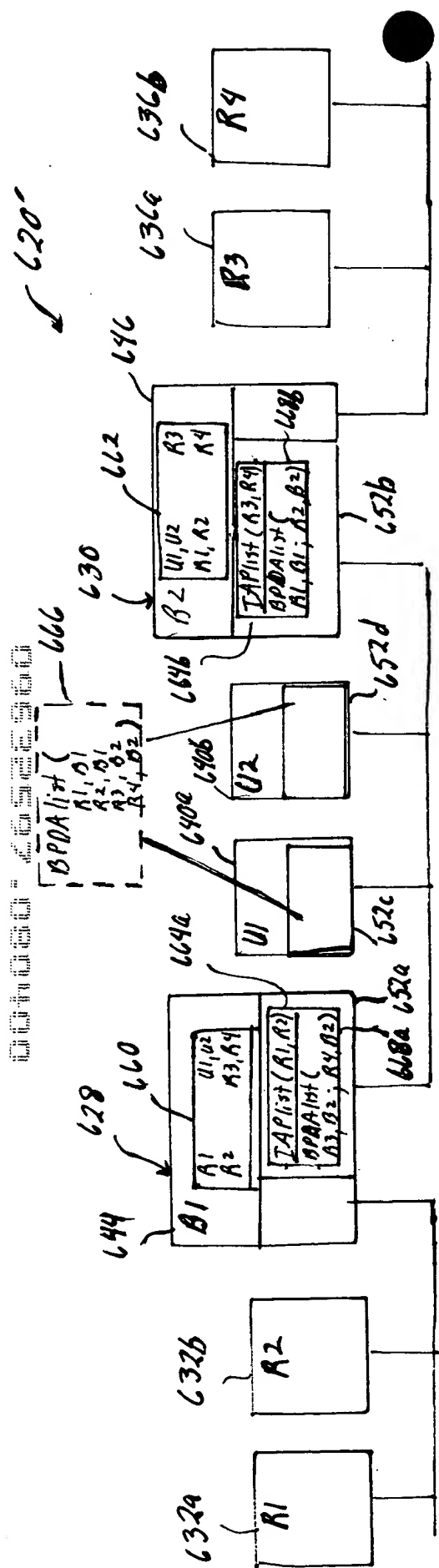


FIG. 33

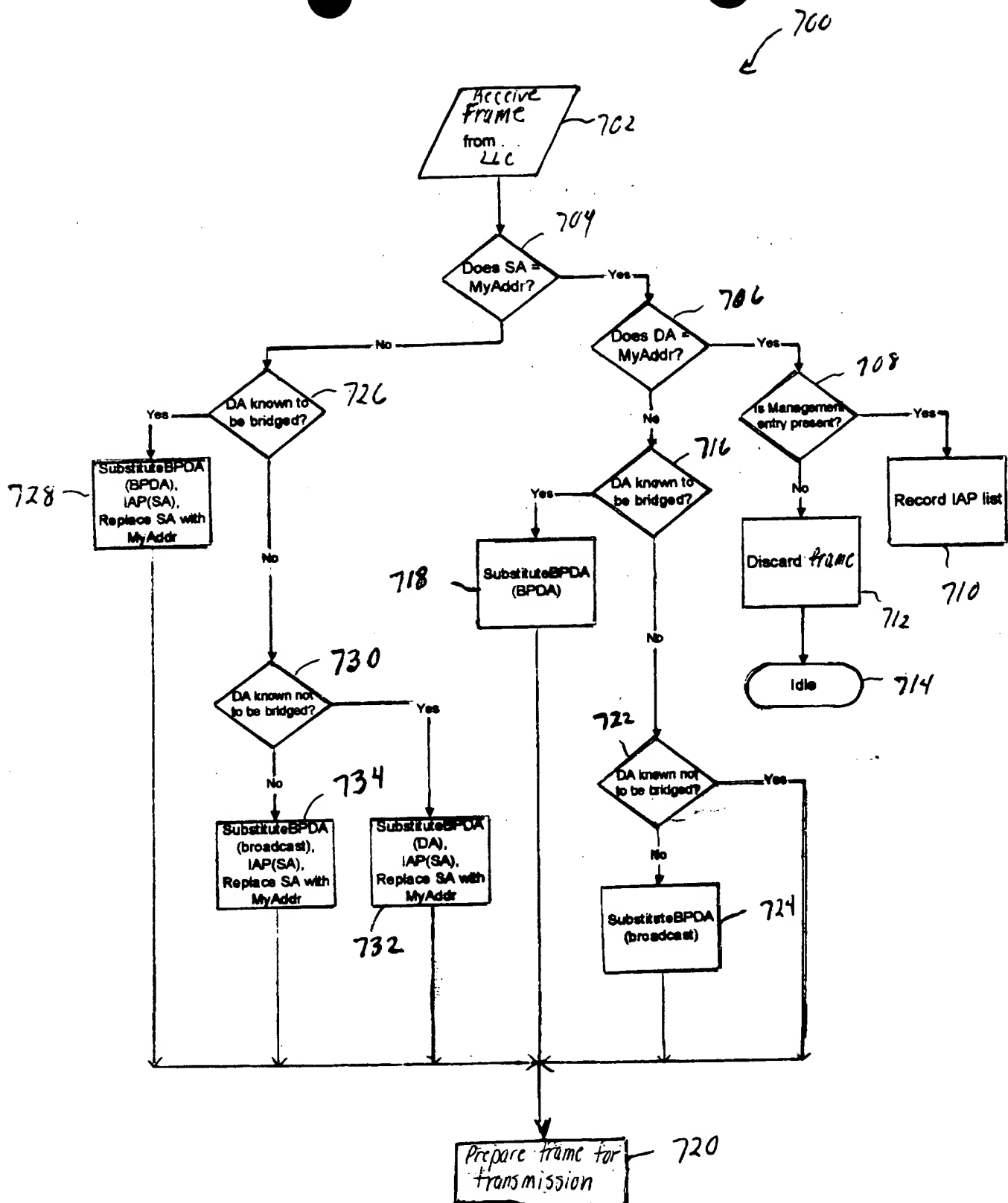


FIG. 34

from FIG. 34

720

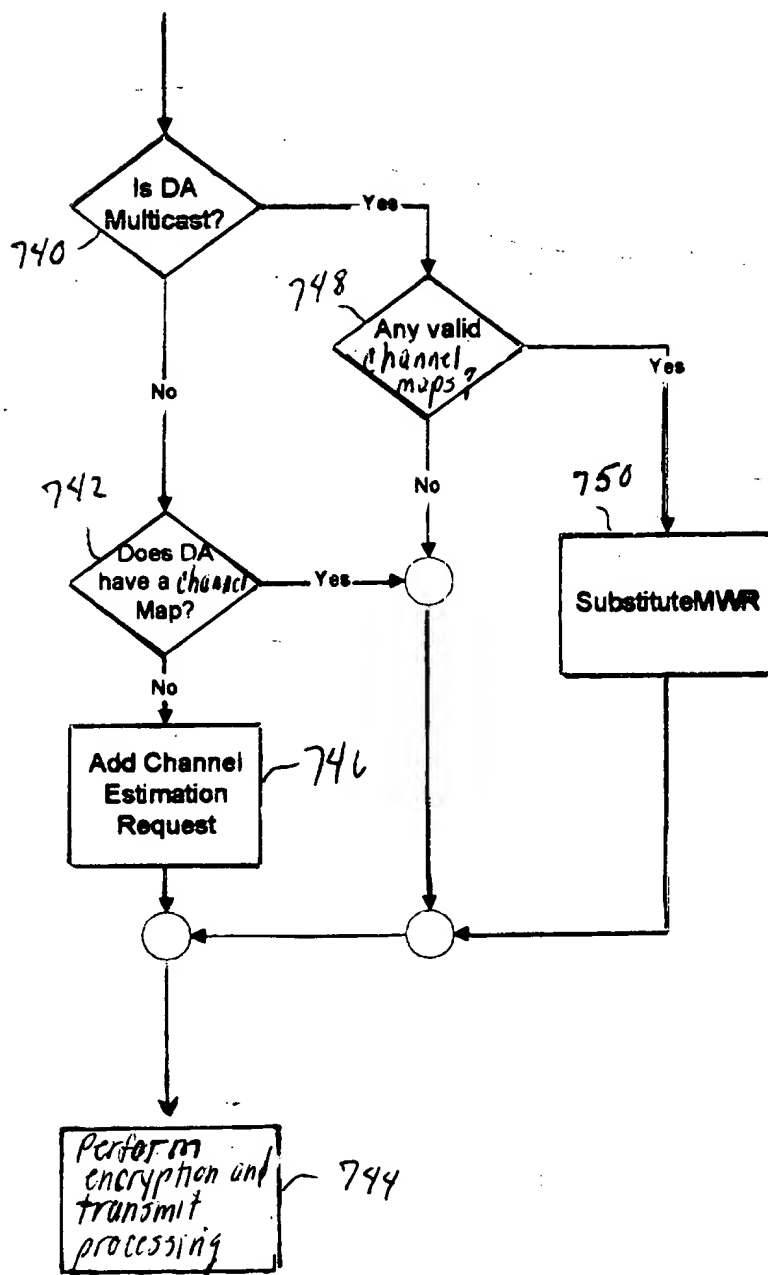


FIG. 35

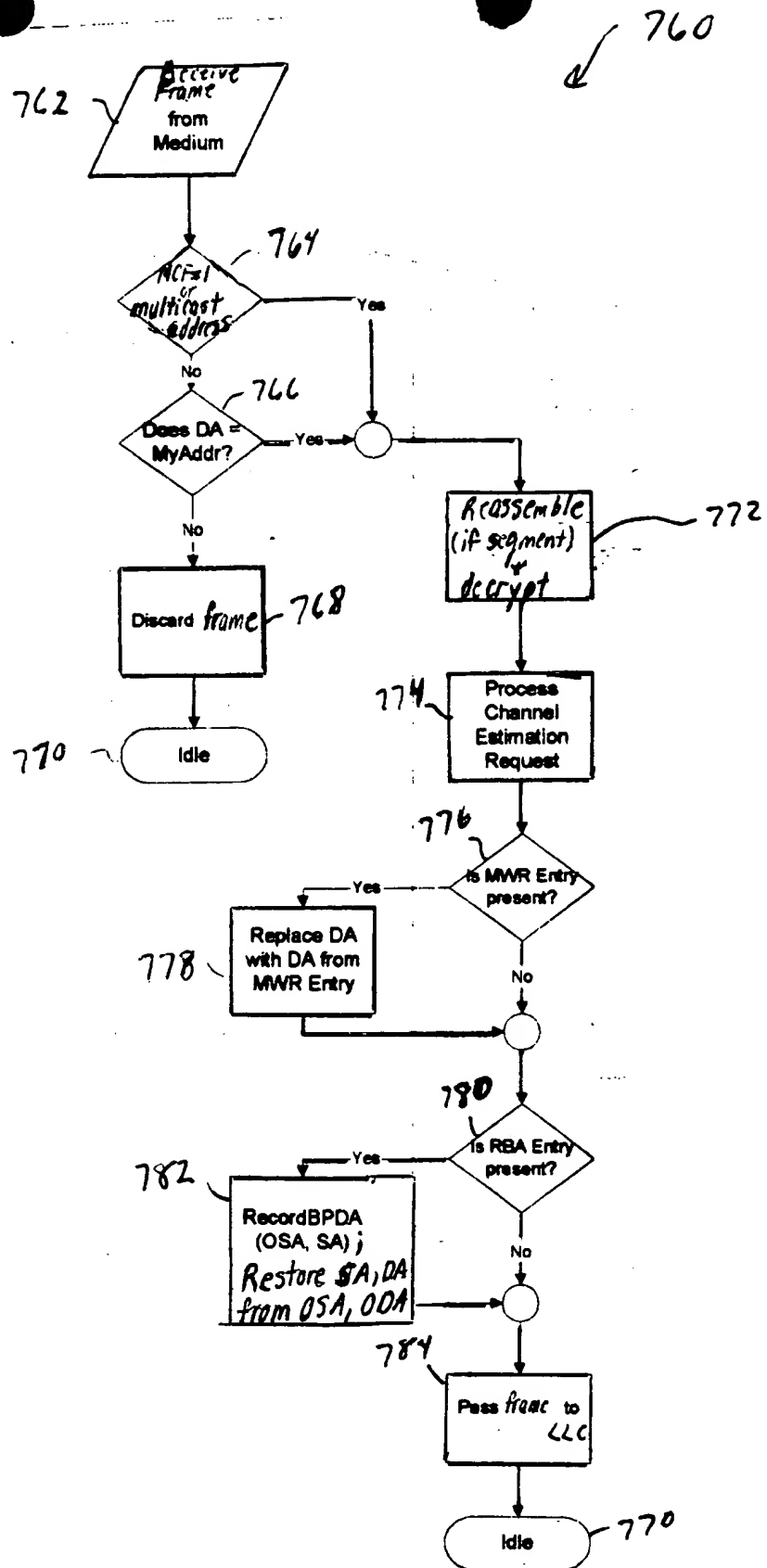


FIG. 36

700

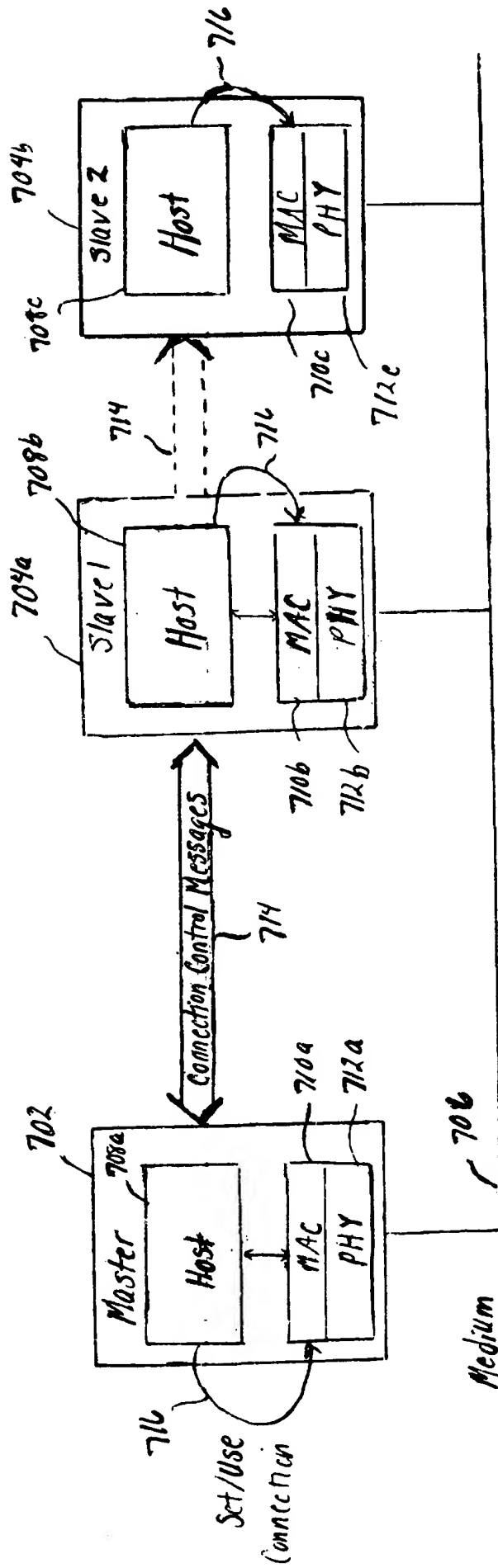


FIG. 37

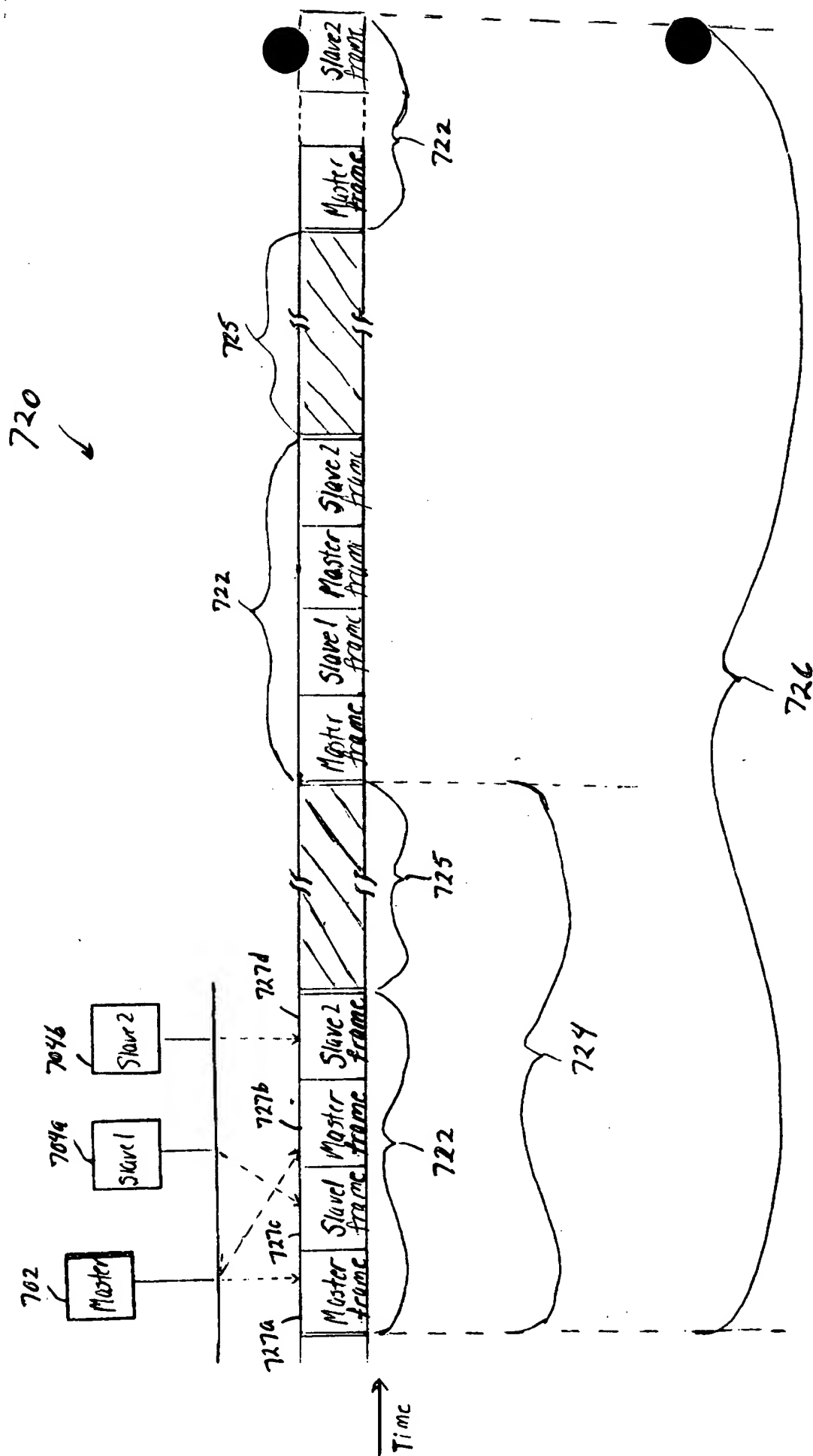
[illegible]

FIG. 38

740

|                   |        |     |               |                |                |               |                    |
|-------------------|--------|-----|---------------|----------------|----------------|---------------|--------------------|
| 744               | 746    | 748 | 750           | 754            | 756            | 752           | 760                |
| Connection Number | Master | SA  | SA Frame Size | Min Frame Time | Max Frame Time | TX Frame Size | Frame Life Control |

FIG. 39A

742

|                   |
|-------------------|
| Connection Number |
|-------------------|

FIG. 39B



800

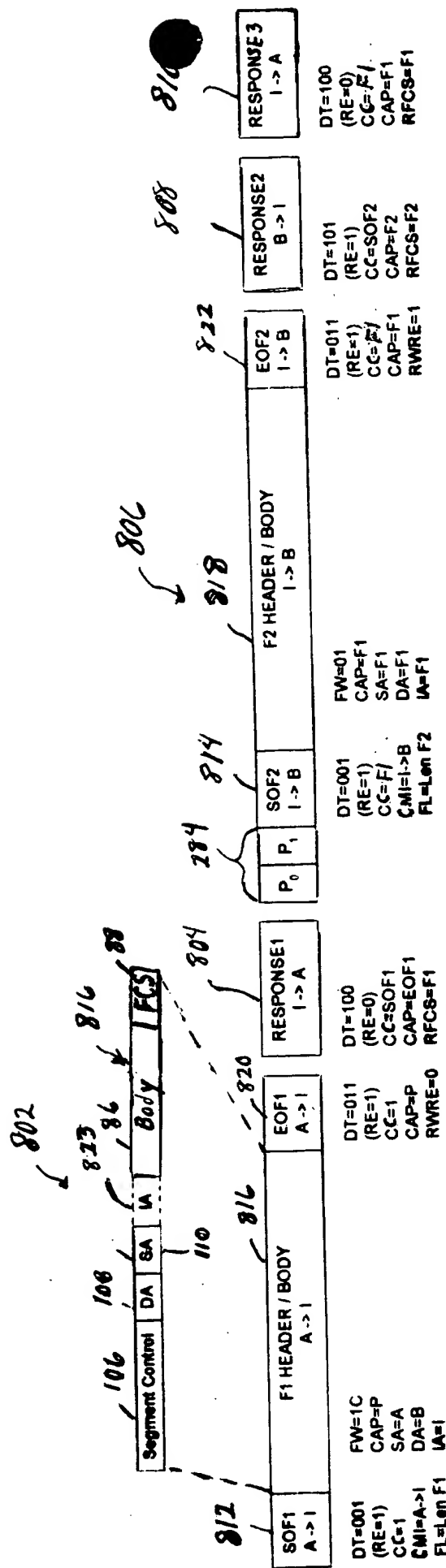


FIG. 90

824

802

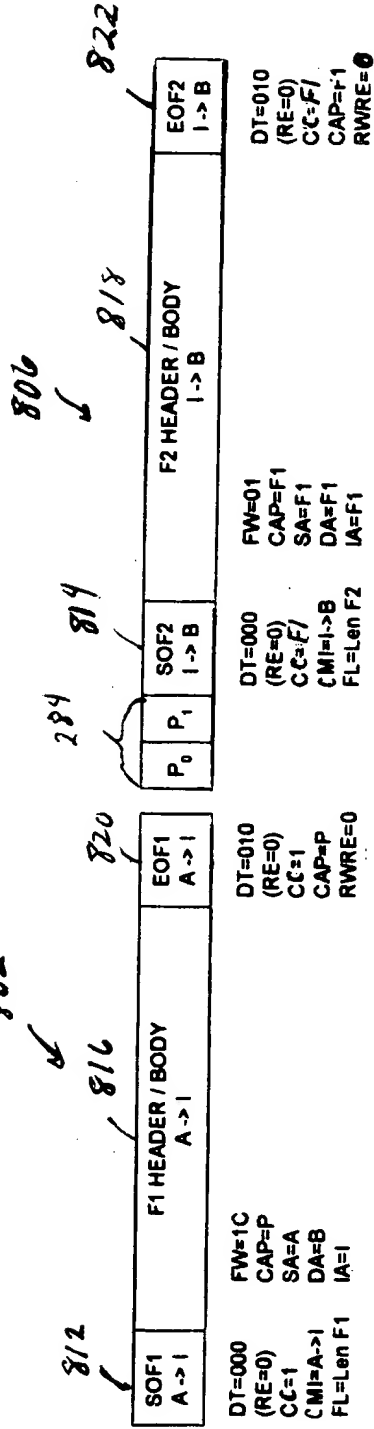


FIG. 41

86  
5

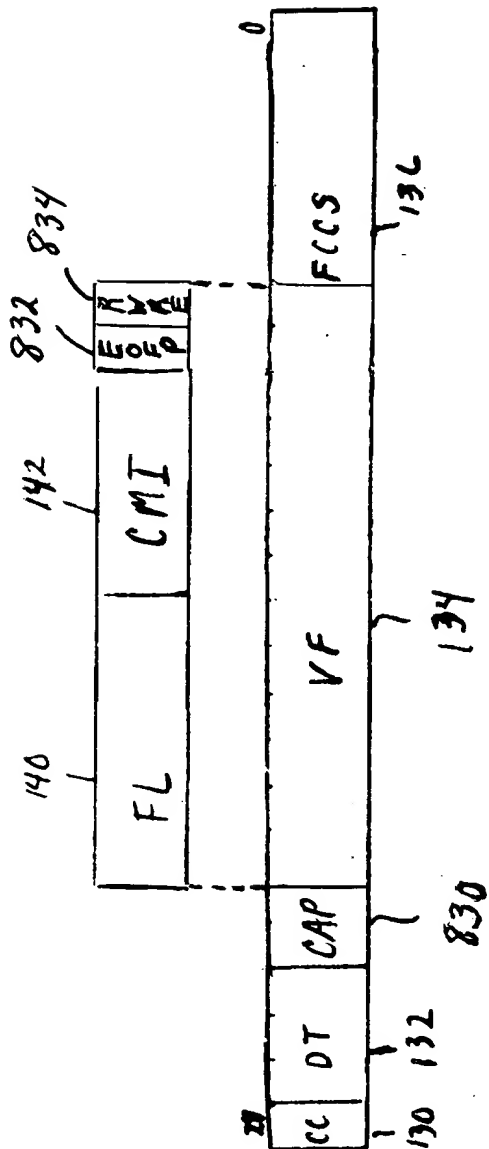


FIG. 42

836

802

806

812

|              |                          |
|--------------|--------------------------|
| SOF1<br>A->1 | F1 HEADER / BODY<br>A->1 |
|--------------|--------------------------|

DT=001  
(RE=1)  
CC=1  
CMI=A->1  
FL=Len F1  
CAP=3  
RWRE=1  
EOFP=0

FW=1C  
CAP=P  
SA=A  
DA=B  
IA=1

816

814

|              |                          |
|--------------|--------------------------|
| SOF2<br>I->B | F2 HEADER / BODY<br>I->B |
|--------------|--------------------------|

DT=000  
(RE=0)  
CC=F1  
CMI=I->B  
FL=Len F2  
CAP=F1  
RWRE=1  
EOFP=0

FW=01  
CAP=F1  
SA=A  
DA=B  
IA=1

818

808

|                   |
|-------------------|
| RESPONSE2<br>B->1 |
|-------------------|

DT=101  
(RE=1)  
CC=SO F2  
CAP=F2  
RFGS=F2

810

|                   |
|-------------------|
| RESPONSE3<br>I->A |
|-------------------|

DT=100  
(RE=0)  
CC=F1  
CAP=F1  
RFGS=F1

FIG. 43

838

804

816

812



DT=001  
(RE=1)  
CC=1  
CMI=A->I  
FL=Len F1  
CAP=3  
RWRE=1  
EOFP=0

FW=1C  
CAP=P  
SA=A  
DA=B  
IA=1

DT=100  
(RE=0)  
CC=F/  
CAP=F1  
ACK=0  
FTYPE=NACK  
or FAIL

FIG. 44

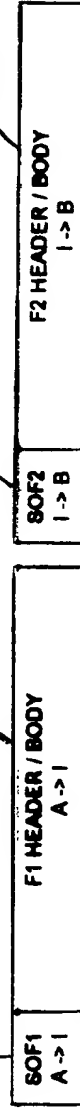
840

812

816

814

818



DT=001  
(RE=1)  
CC=1  
CMI=A->I  
FL=Len F1  
CAP=3  
RWRE=0  
EOFP=0

FW=1C  
CAP=P  
SA=A  
DA=B  
IA=1

DT=000  
(RE=0)  
CC=F/  
CMI=I->B  
FL=Len F2  
CAP=F1  
RWRE=0  
EOFP=0

FW=01  
CAP=F1  
SA=A  
DA=B  
IA=1

FIG. 45

102

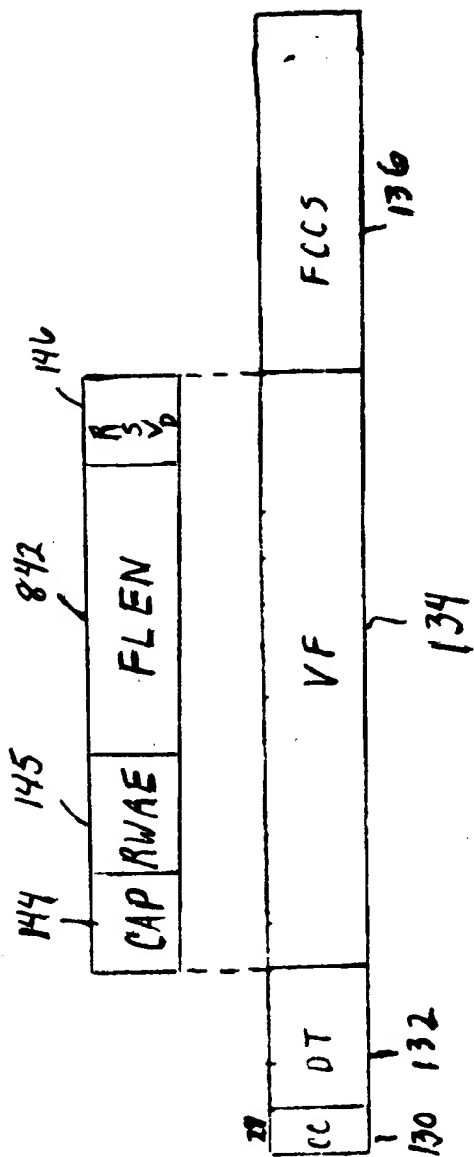


FIG. 46